



MP0367

12/18/04

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Chen et al.

Group Art Unit: Not Yet Assigned

Serial No.: 10/672774

Examiner: Not Yet Assigned

Filed: September 26, 2003

Title: Data Processing System with Partial Bypass Reorder Buffer and Combined Load/Store Arithmetic Logic Unit and Processing Method Thereof

CERTIFICATE OF MAILING

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Date: 12-22-04

Stephanie Sletten

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Respectfully submitted,

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Date: 12/3/04

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PTO/SB/21 (04-04)

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FORM**

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/672774
	Filing Date	September 26, 2003
	First Named Inventor	Chen, Hong-Yi
	Art Unit	To Be Assigned
	Examiner Name	To Be Assigned
Total Number of Pages in This Submission	Attorney Docket Number	MP0367

ENCLOSURES (check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Harness, Dickey & Pierce, P.L.C.	Attorney Name	Michael D. Wiggins	Reg. No.	34,754
Signature					
Date	December 22, 2004				

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Signature		Date	December 22, 2004

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